

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent of: Aronson et al.

U.S. Patent No.: 7,079,775 B2

Application Serial No. 09/777,917

Issued: July 18, 2006

Filed: February 5, 2001

For: *Integrated Memory Mapped
Controller Circuit for Fiber
Optics Transceiver*

Attorney Docket No.: 60900-0052-US

REQUEST FOR EXPEDITED CERTIFICATE OF CORRECTION

MAIL STOP CERTIFICATE OF CORRECTIONS BRANCH

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. 1.322 and MPEP 1480.01 applicants request the expedited issuance of a Certificate of Correction as the error is attributed solely to the Office. Upon review of the issued patent, applicants discovered an error with Claim 62. An amendment was filed on March 22, 2004, which shows the correct claim. Despite the amendment being listed in PAIR, the Patent issued with the incorrect claim. As such, applicants request correction of the issued patent as outlined in the attached Certificate of Correction (form PTO 1050).

The Commissioner has been authorized through the electronic filing system to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0310 (order No. 060900-0052-US).

Date: May 13, 2008

Respectfully submitted,


Dion M. Bregman

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(Reg. No.)

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,079,775 B2
 APPLICATION NO. : 09/777,917
 ISSUE DATE : July 18, 2006
 INVENTOR(S) : Aronson et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 25, Claim 62, Line 25, delete --to read from host--

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